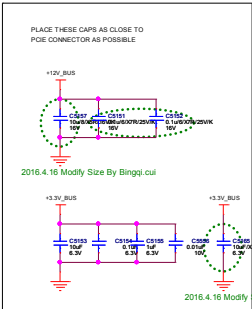


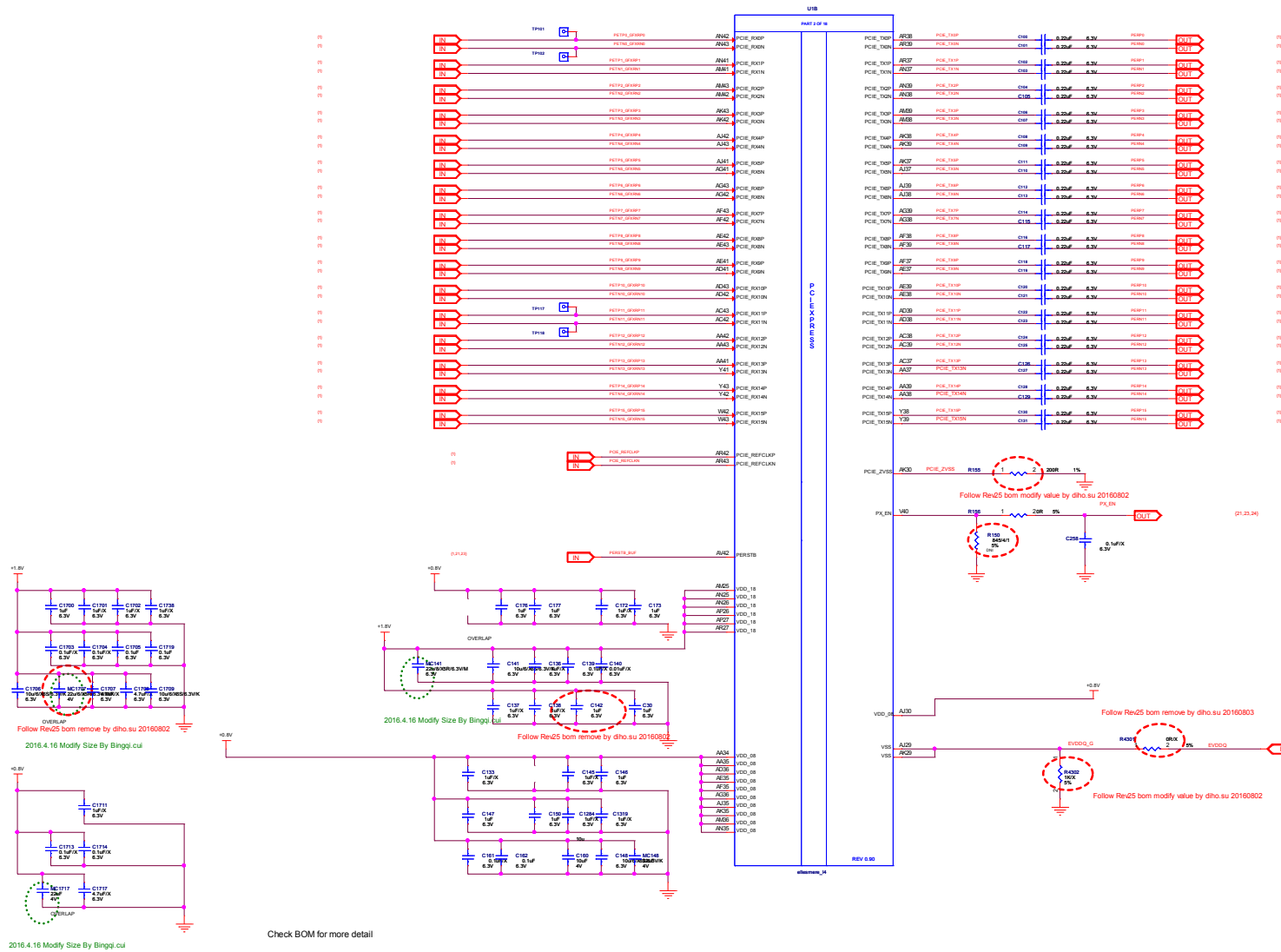


(1) PCI-EXPRESS EDGE CONNECTOR

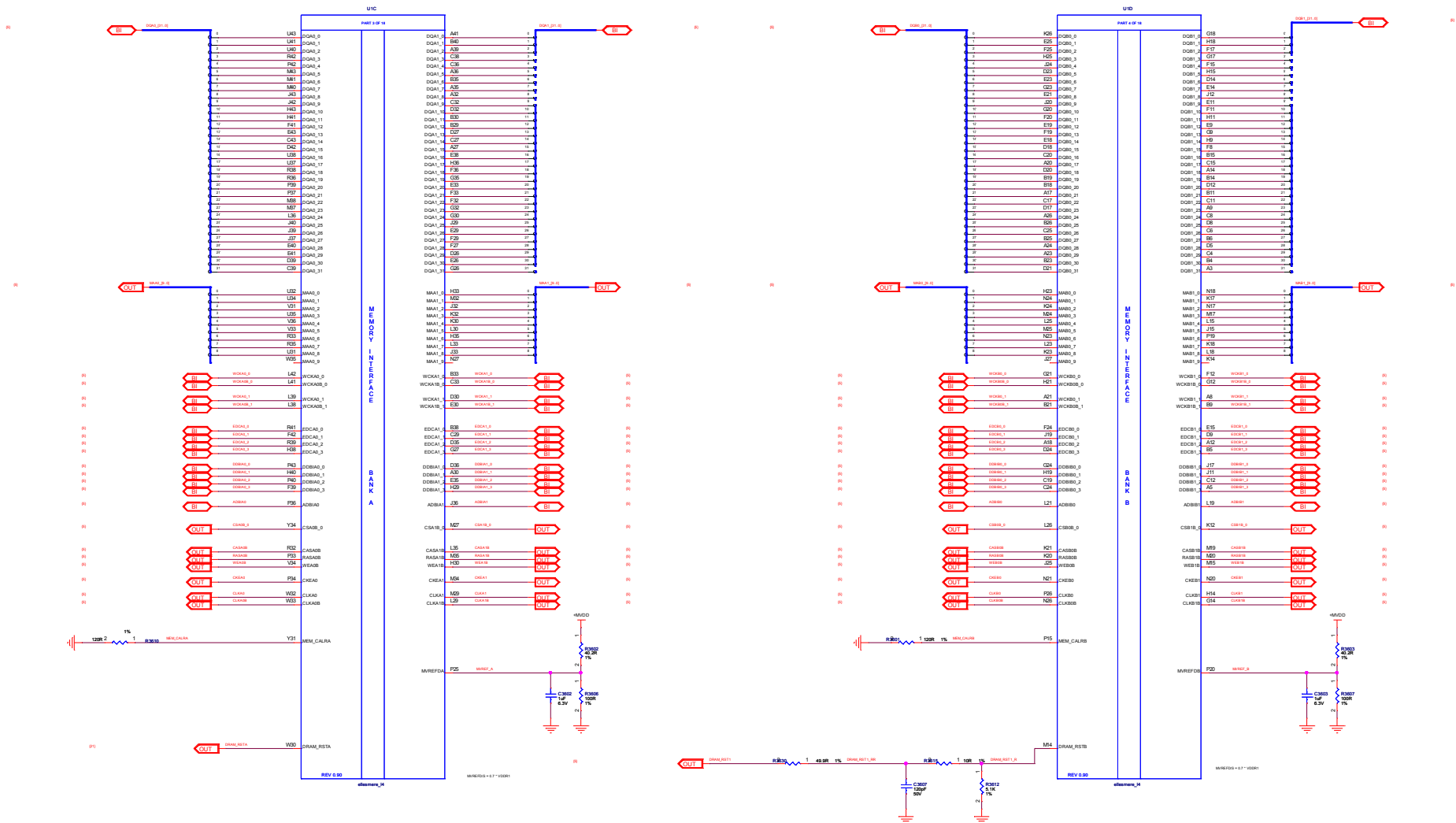


SYMBOL LEGEND	
ONE	DON'T KNOW
0 or #	ACTIVE LOW
BUG	BURNS UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

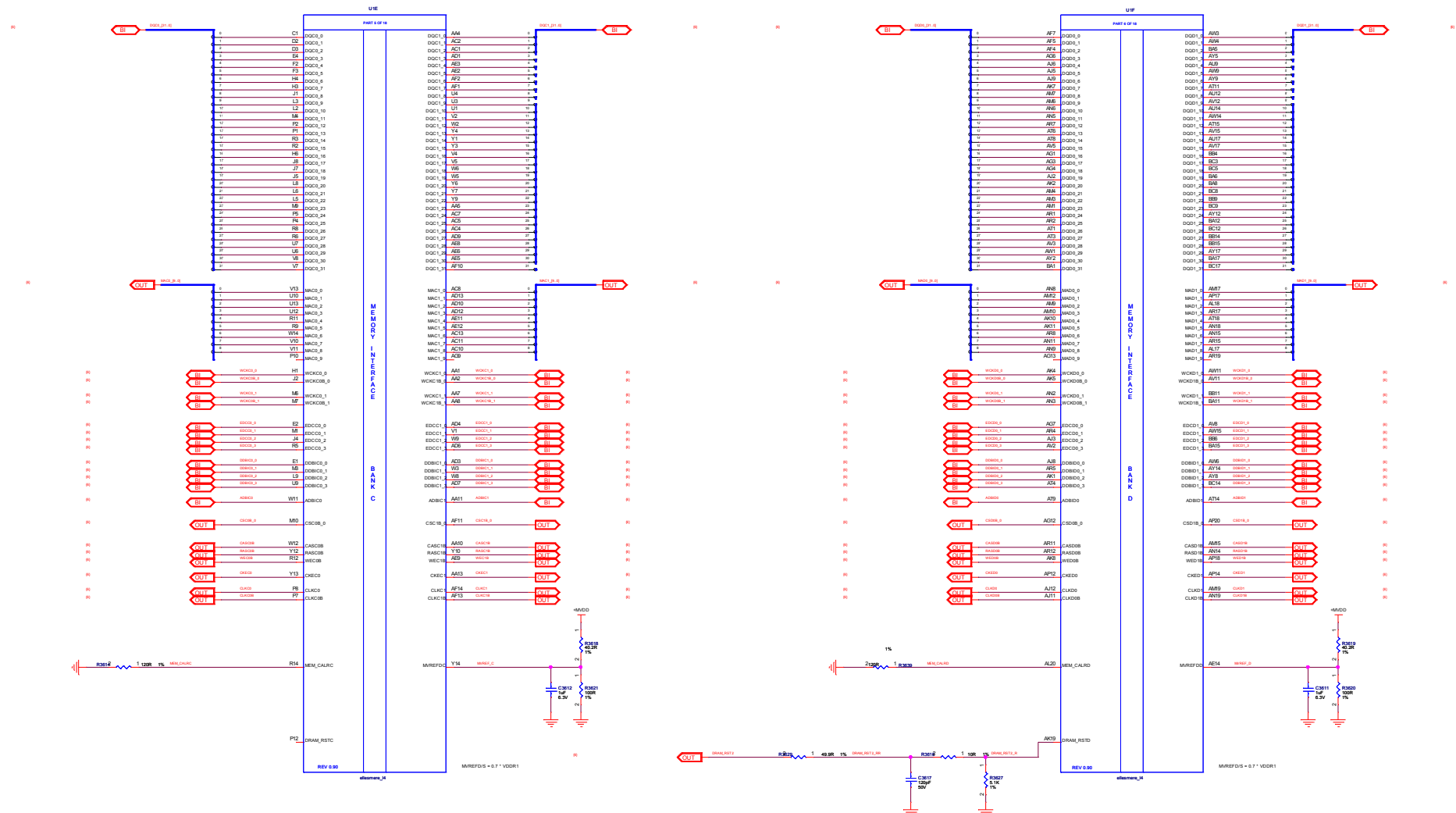
(2) ELLESMERE PCIE INTERFACE



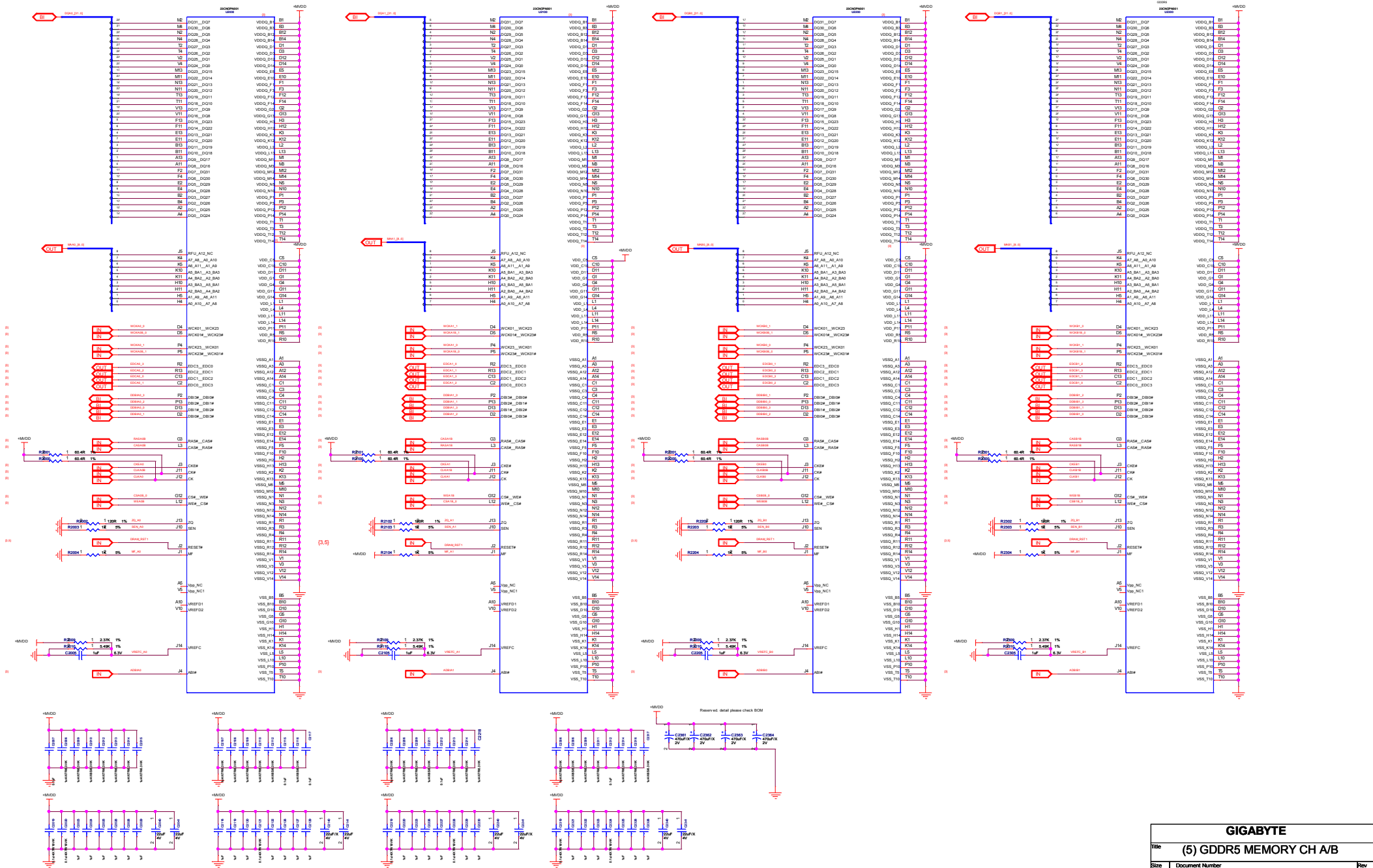
(3) ELLESMERE MEM INTERFACE CH A/B



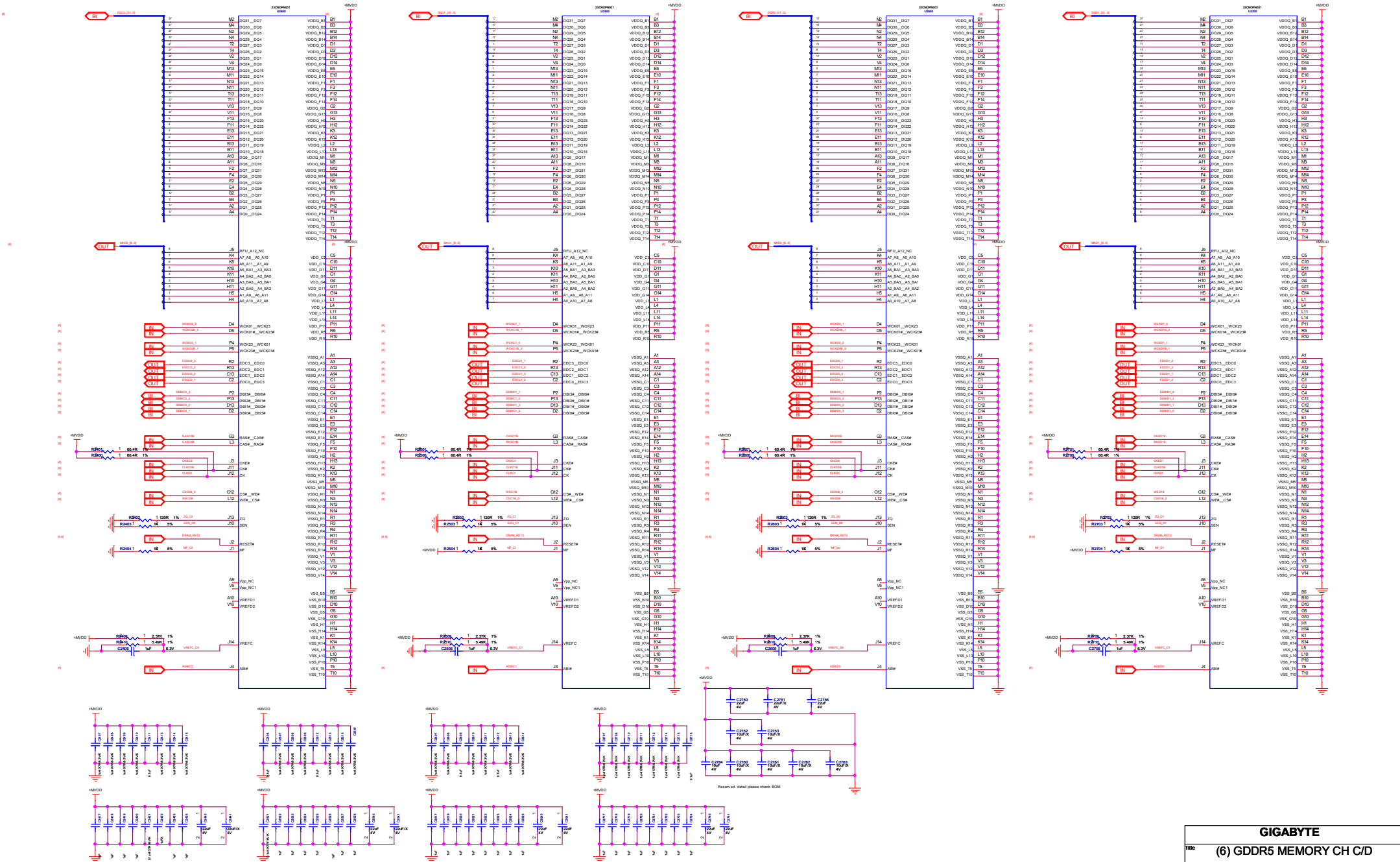
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B



(6) GDDR5 MEMORY CH C/D

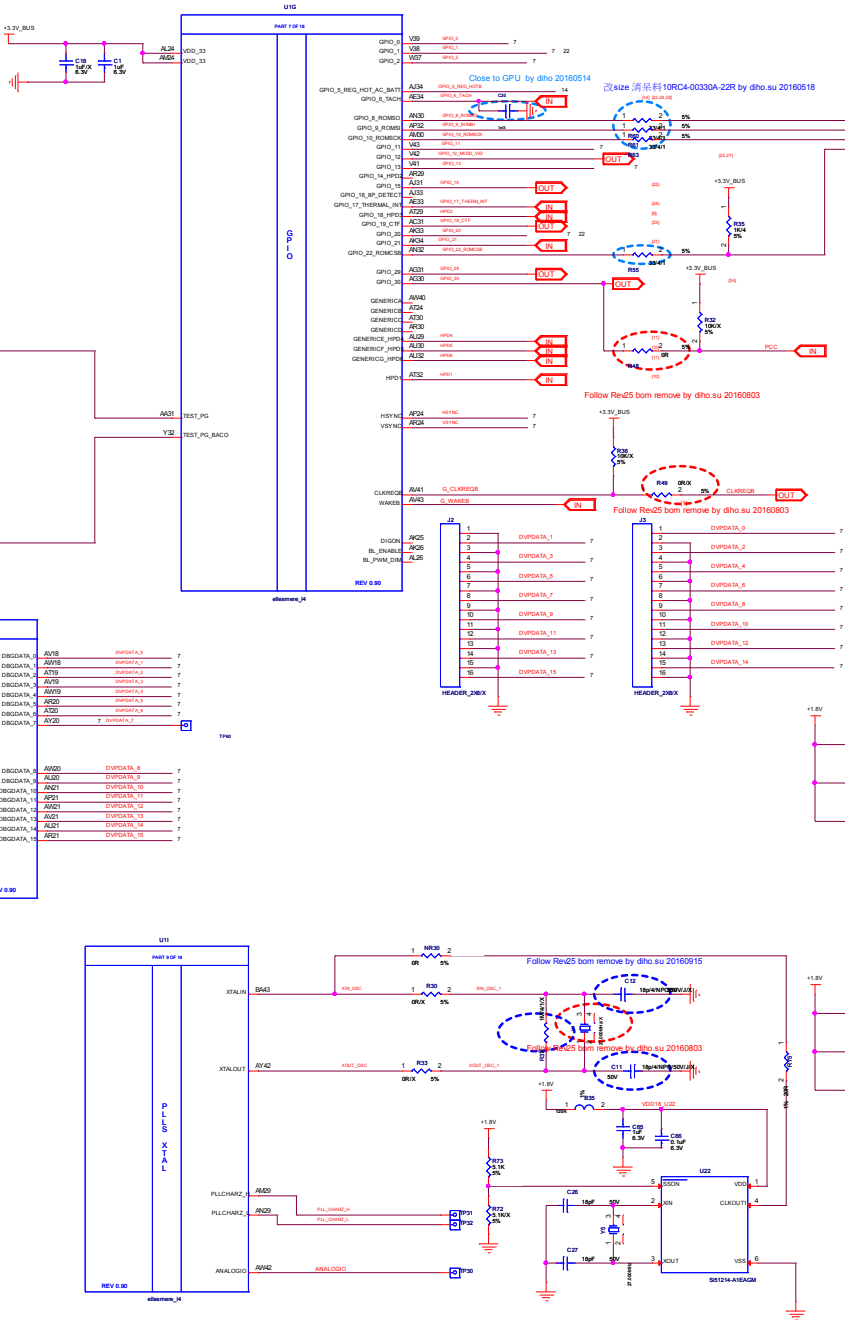
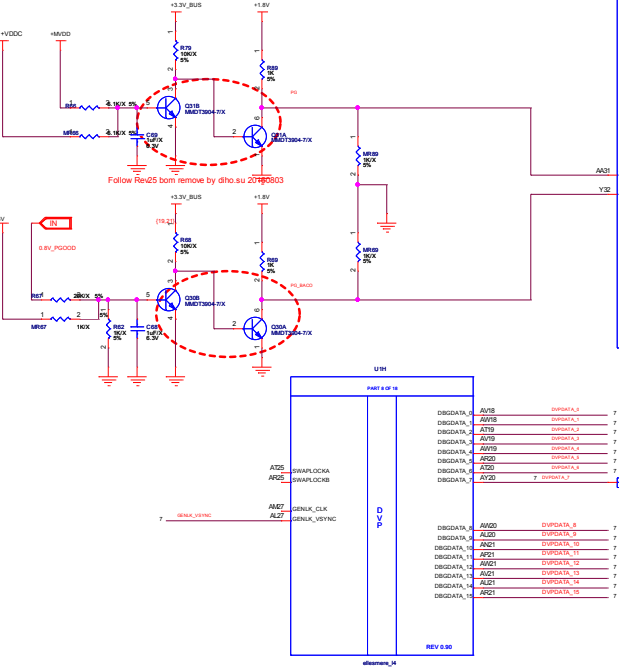


(7) ELLESMERE GPIO STRAP CF XTAL

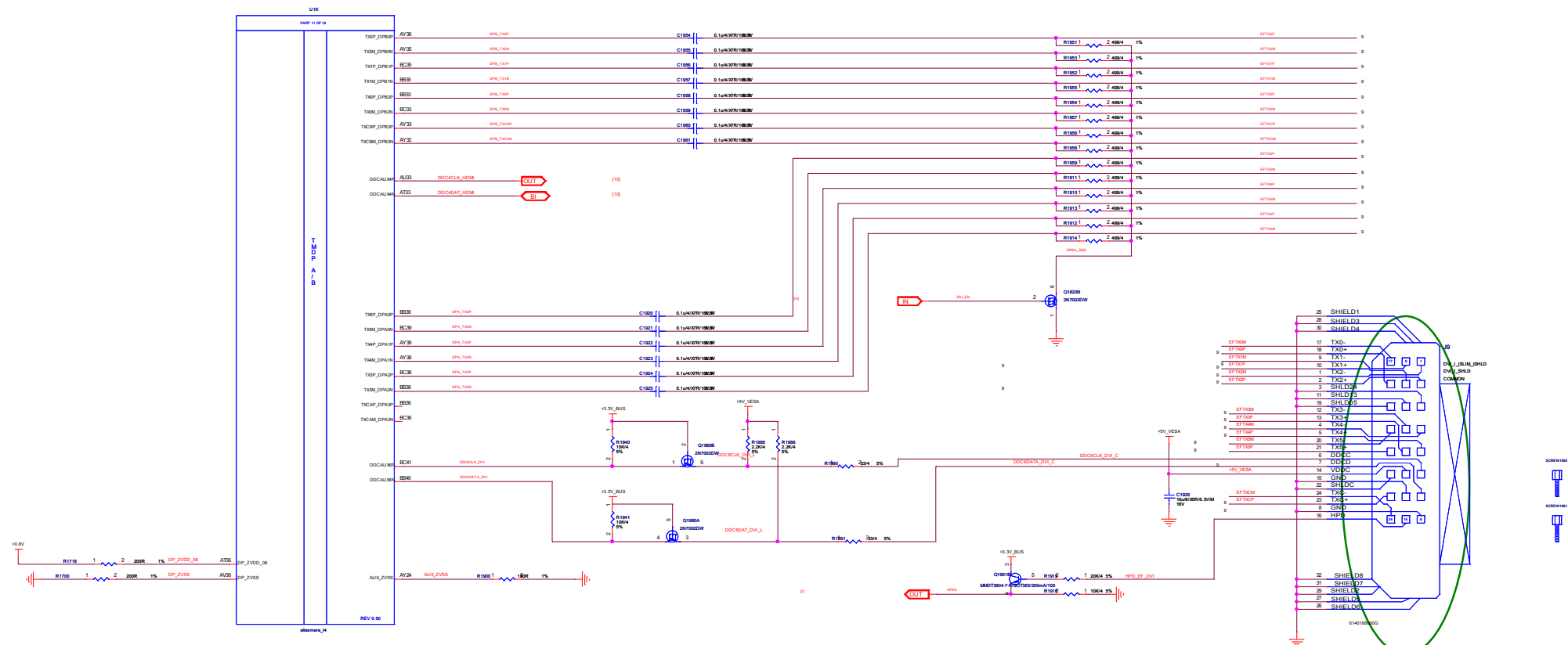
12C ADDRESS	FUNCTION	DEVICE

DDCVGA BUS:

12C ADDRESS	FUNCTION	DEVICE
0499	EXT TEMP SENSOR	LM8993

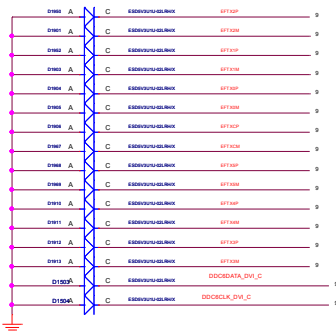


(9) ELLESMERE TMDP A/B

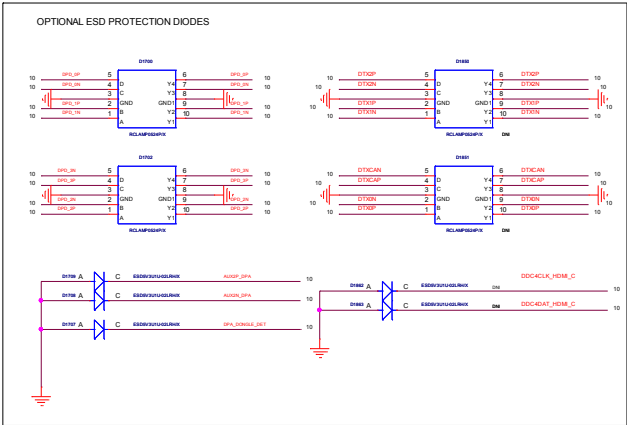
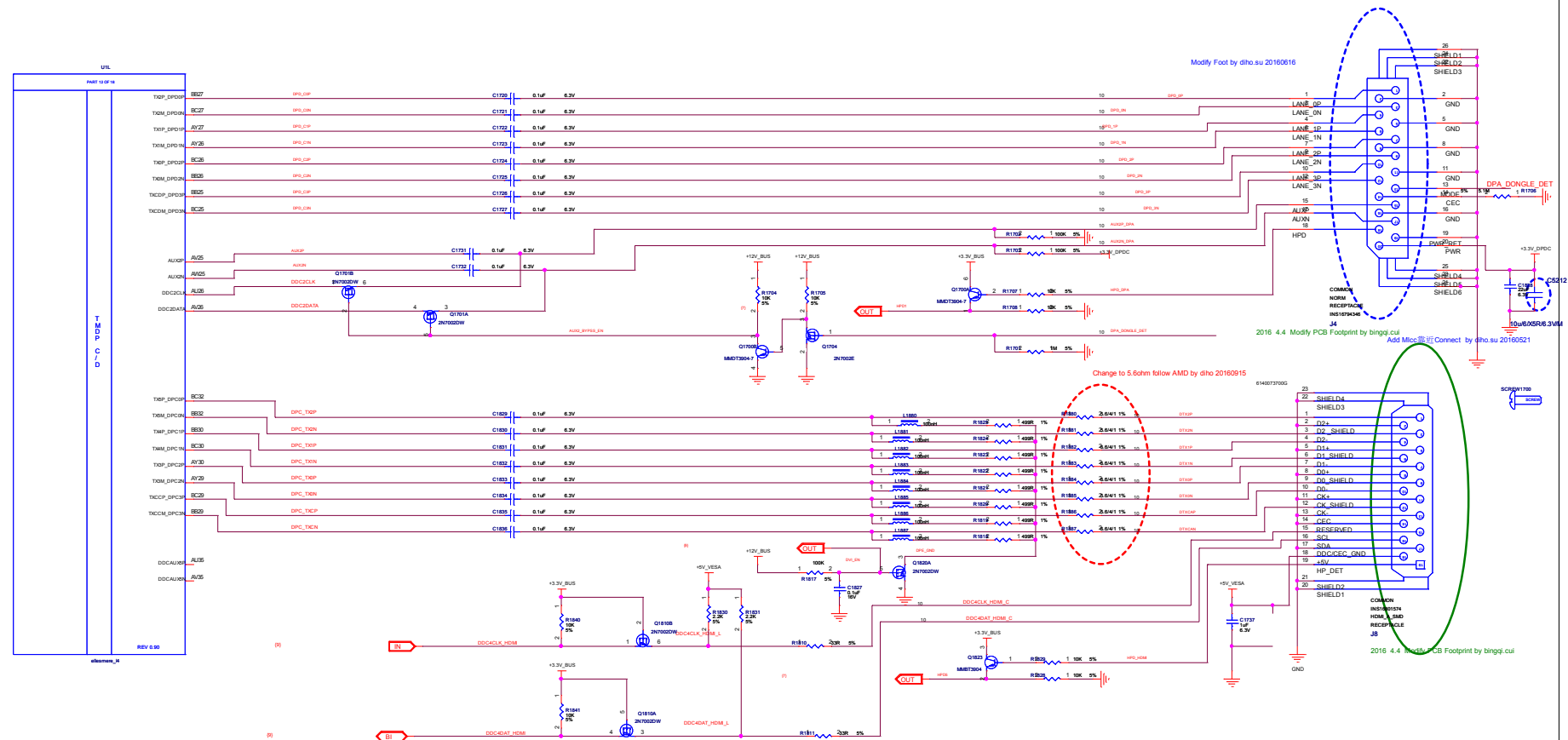


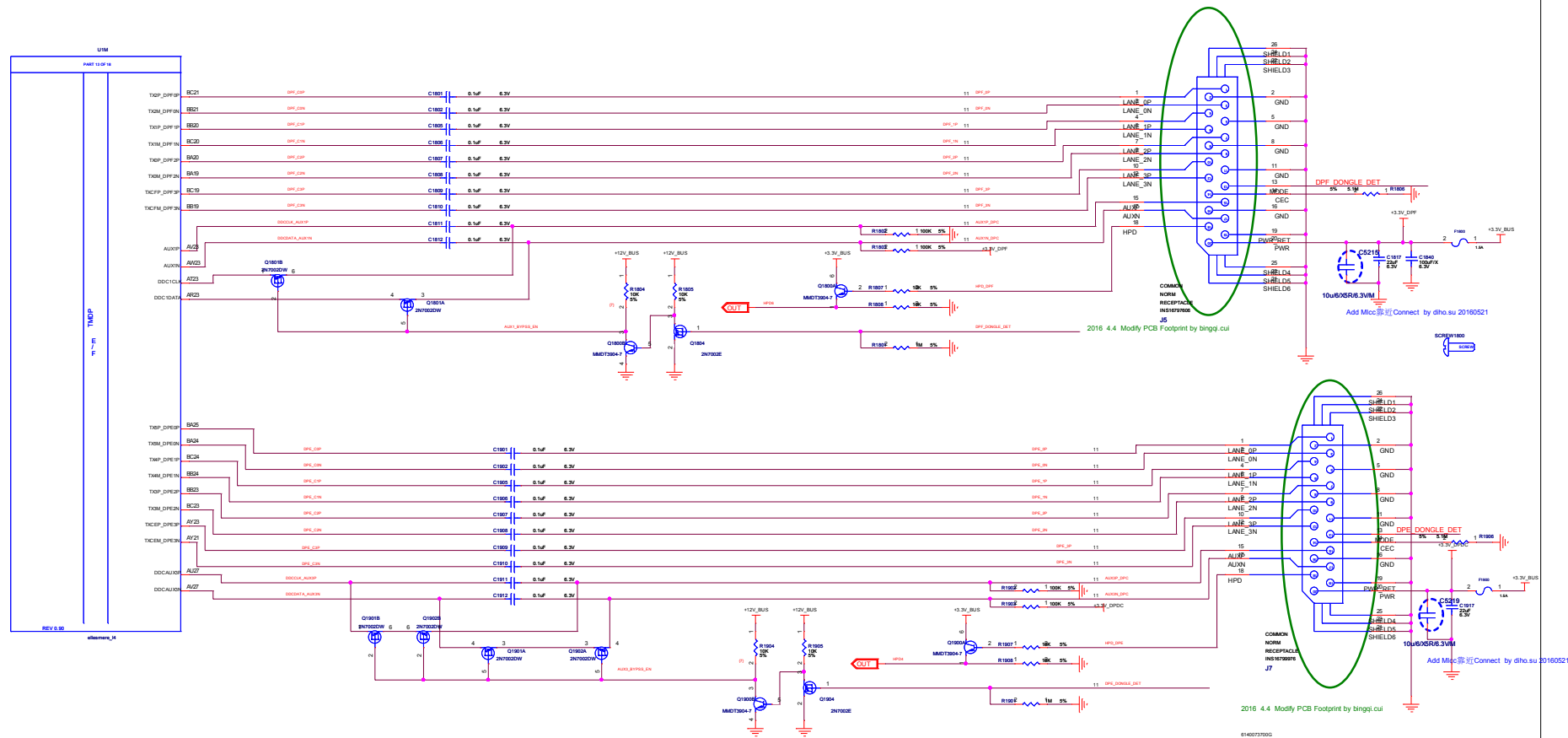
2016 5.20 Modify PCB Footprint by DIHO.SU

OPTIONAL ESD PROTECTION DIODES

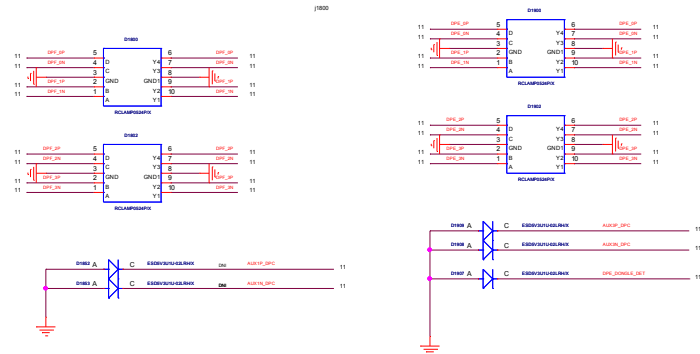


(10) ELLESMERE TMDP C/D

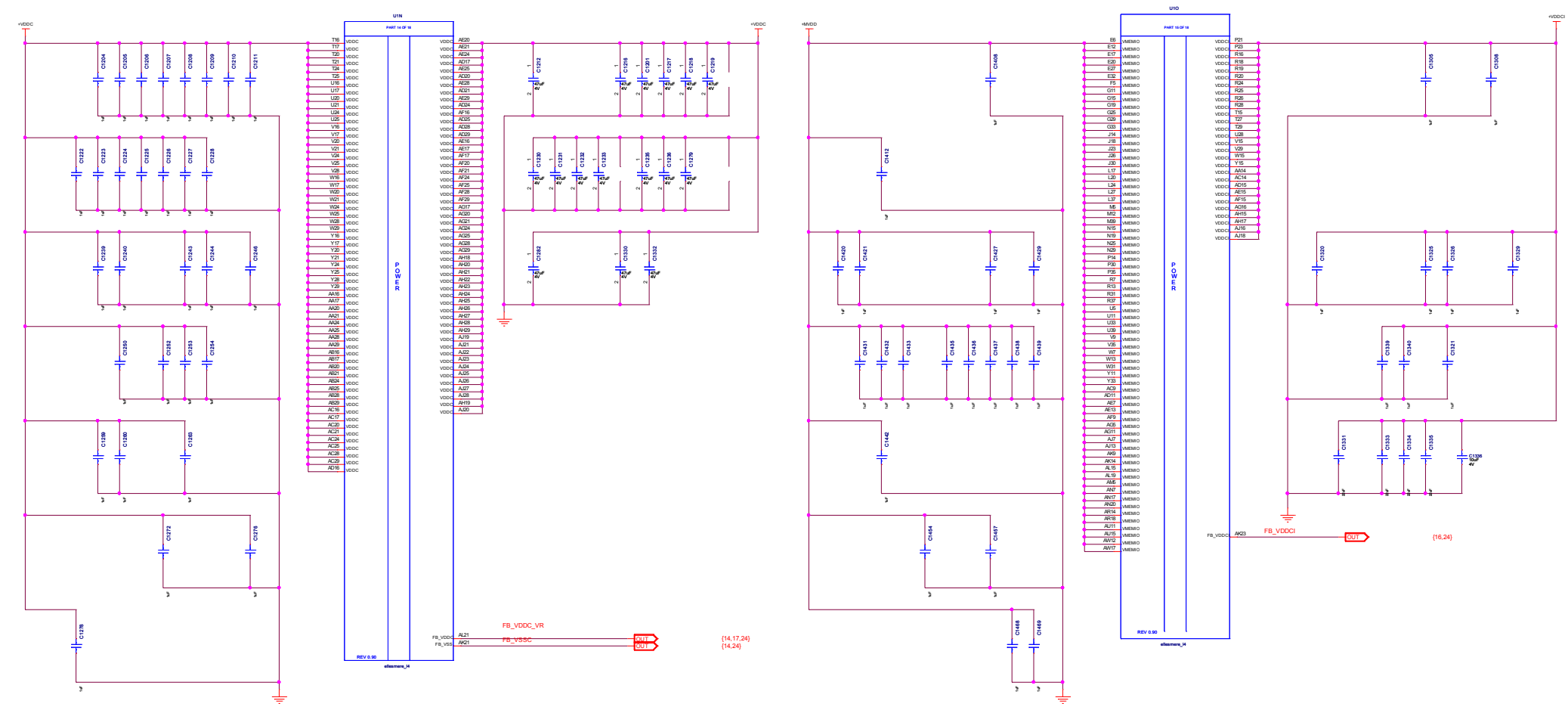




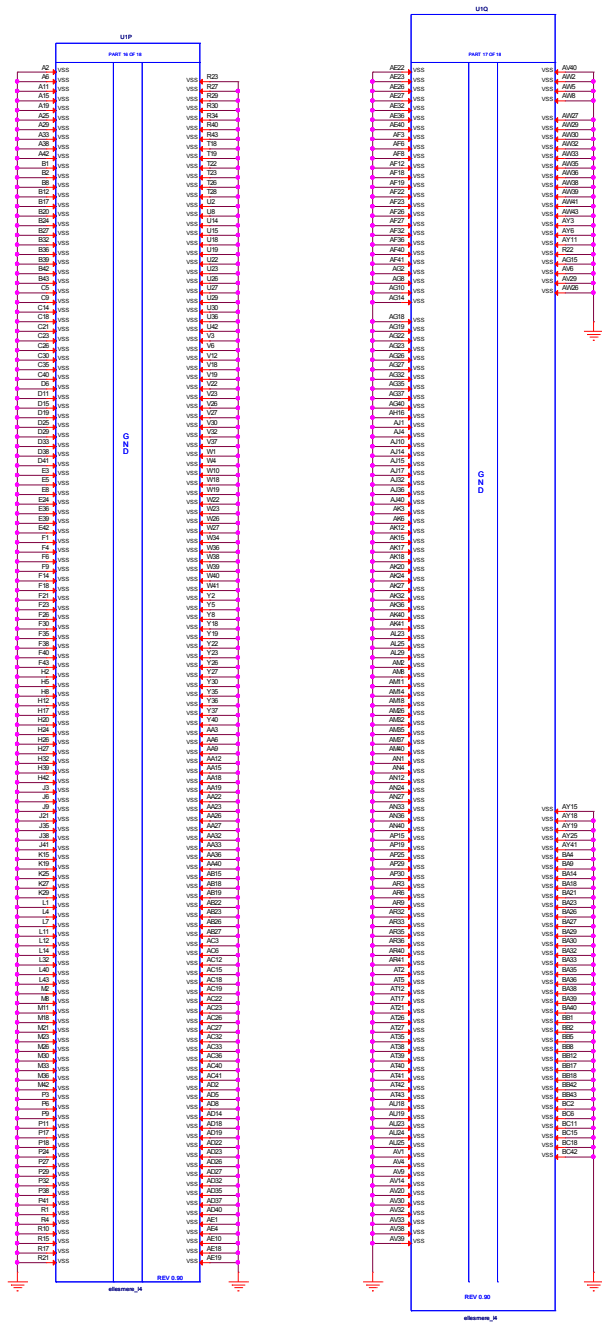
OPTIONAL ESD PROTECTION DIODES

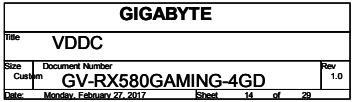


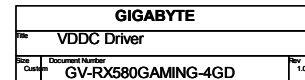
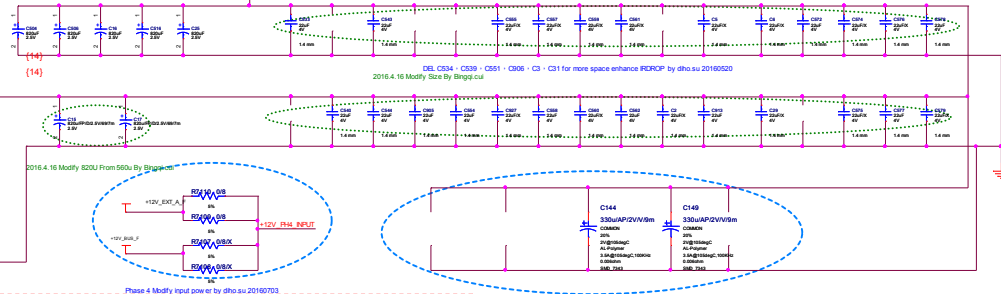
(12) ELLESMERE POWER

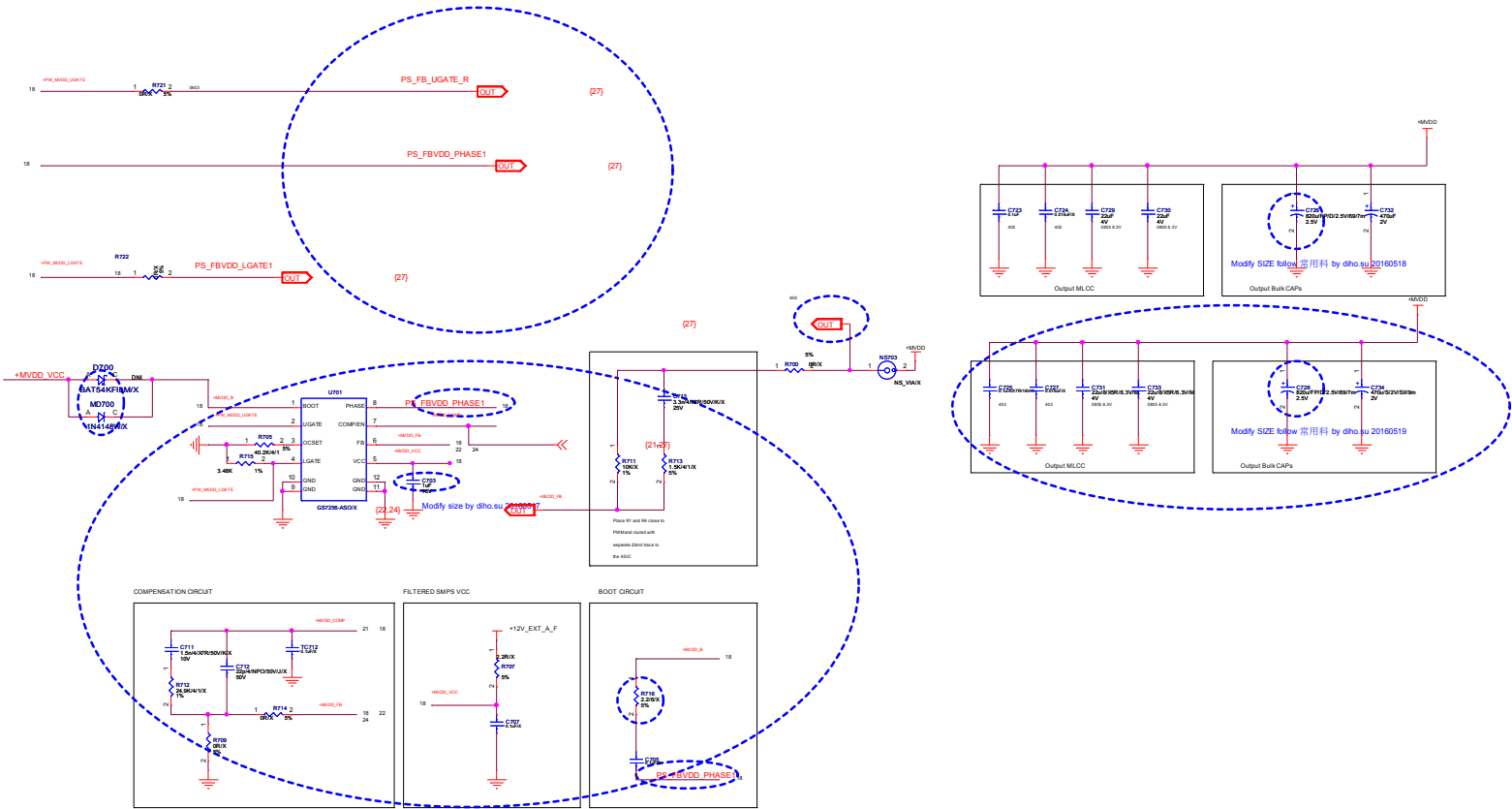


(13) ELLESMERE GROUND



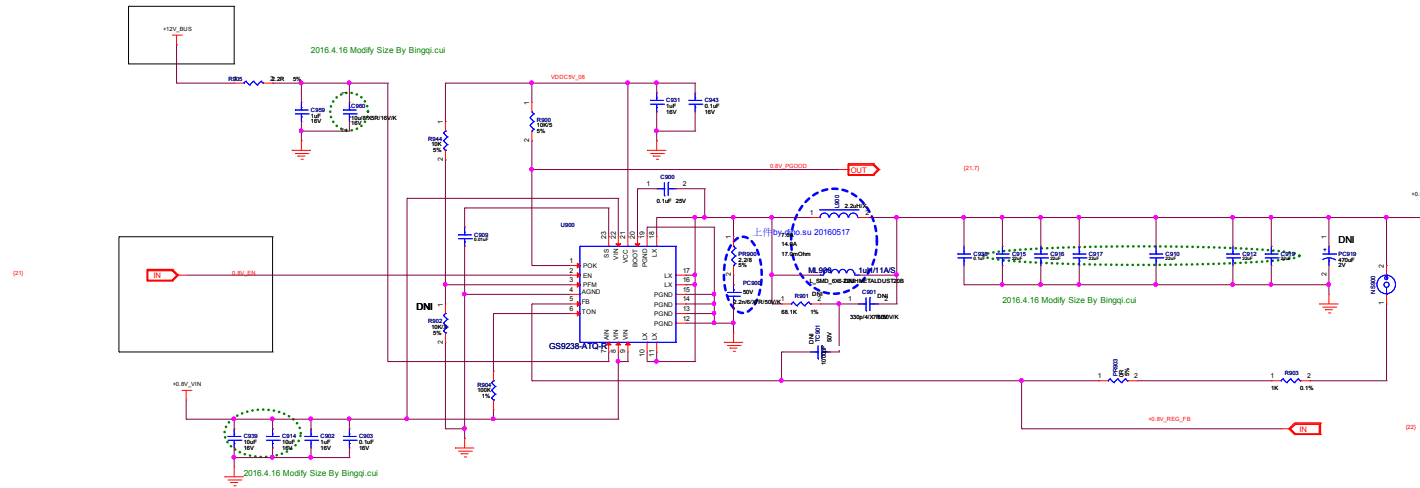






/X Single phase power by diho.su 20160517

(17) 0.95V



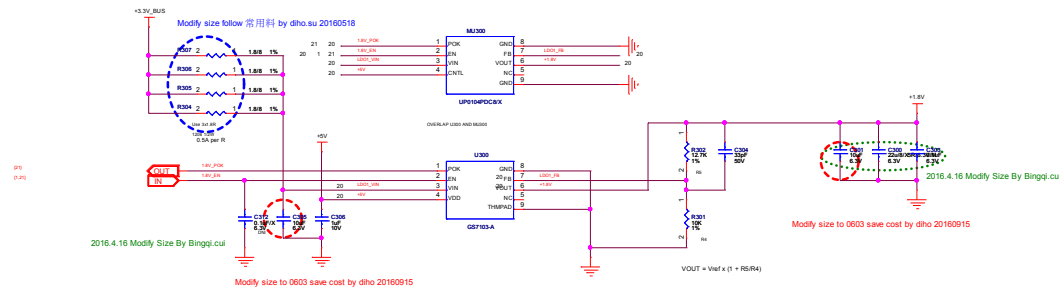
(18) SMALL RAIL REGULATORS

LDO #1:VIN = 3.0V TO 3.6V MAX

VOUT = +1.8V +/- 2%

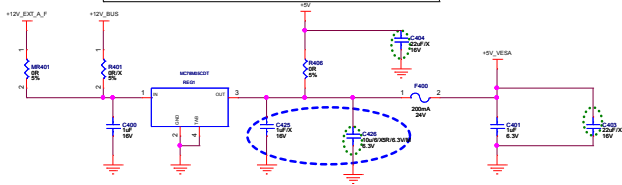
IOUT = 1.3A RMS MAX

PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

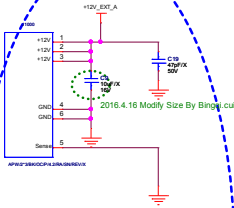


REGULATOR FOR +5V RAILS

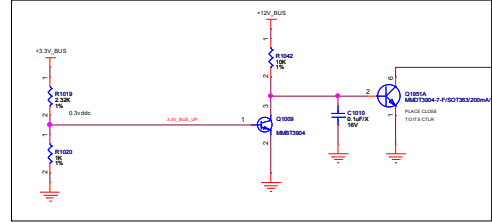
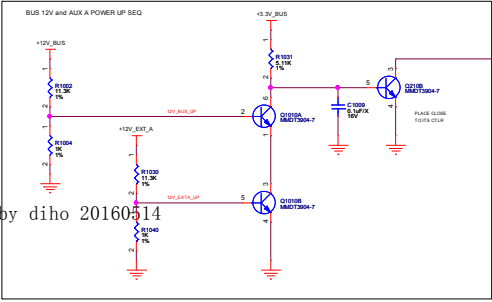
IOUT MAX = 150mA



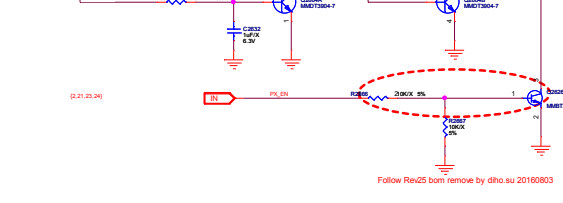
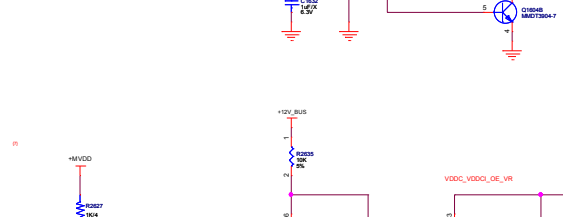
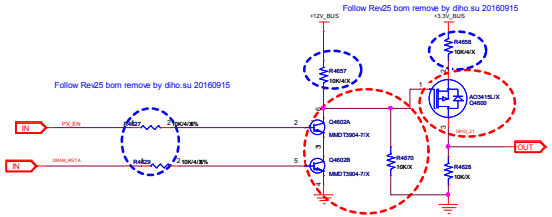
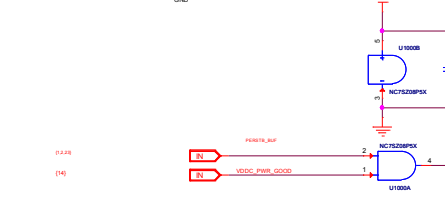
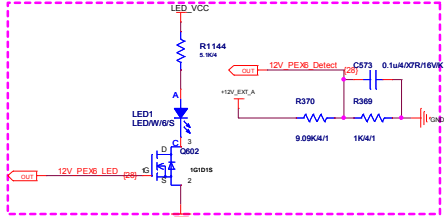
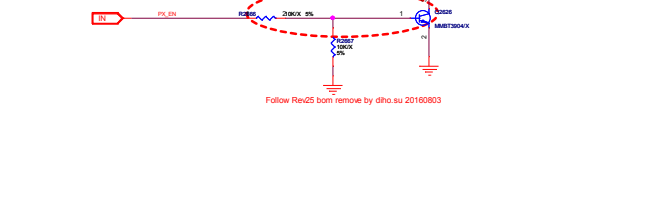
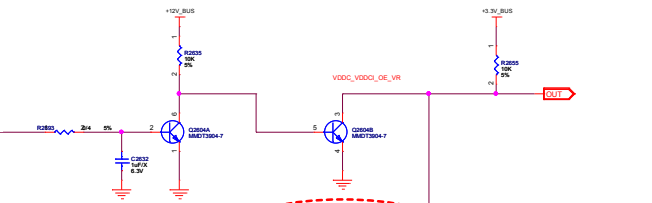
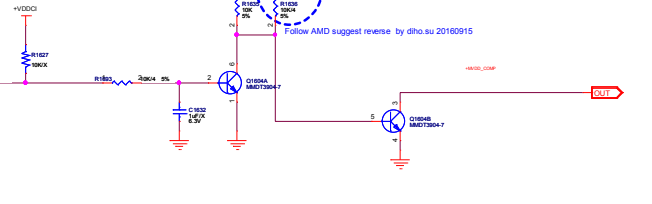
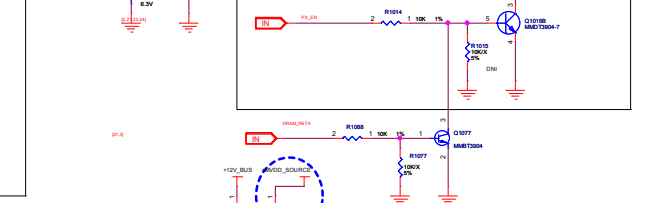
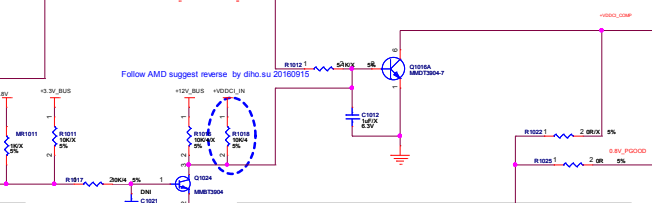
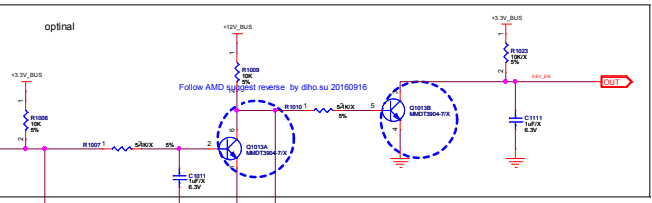
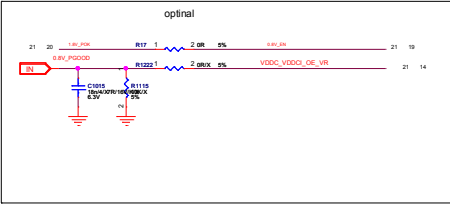
(19) POWER MANAGEMENT



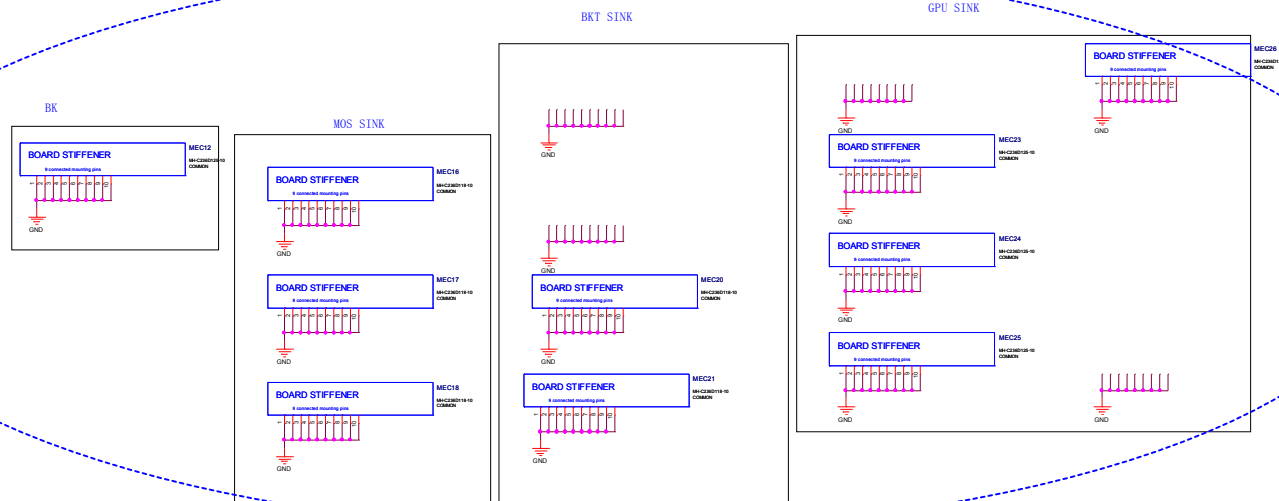
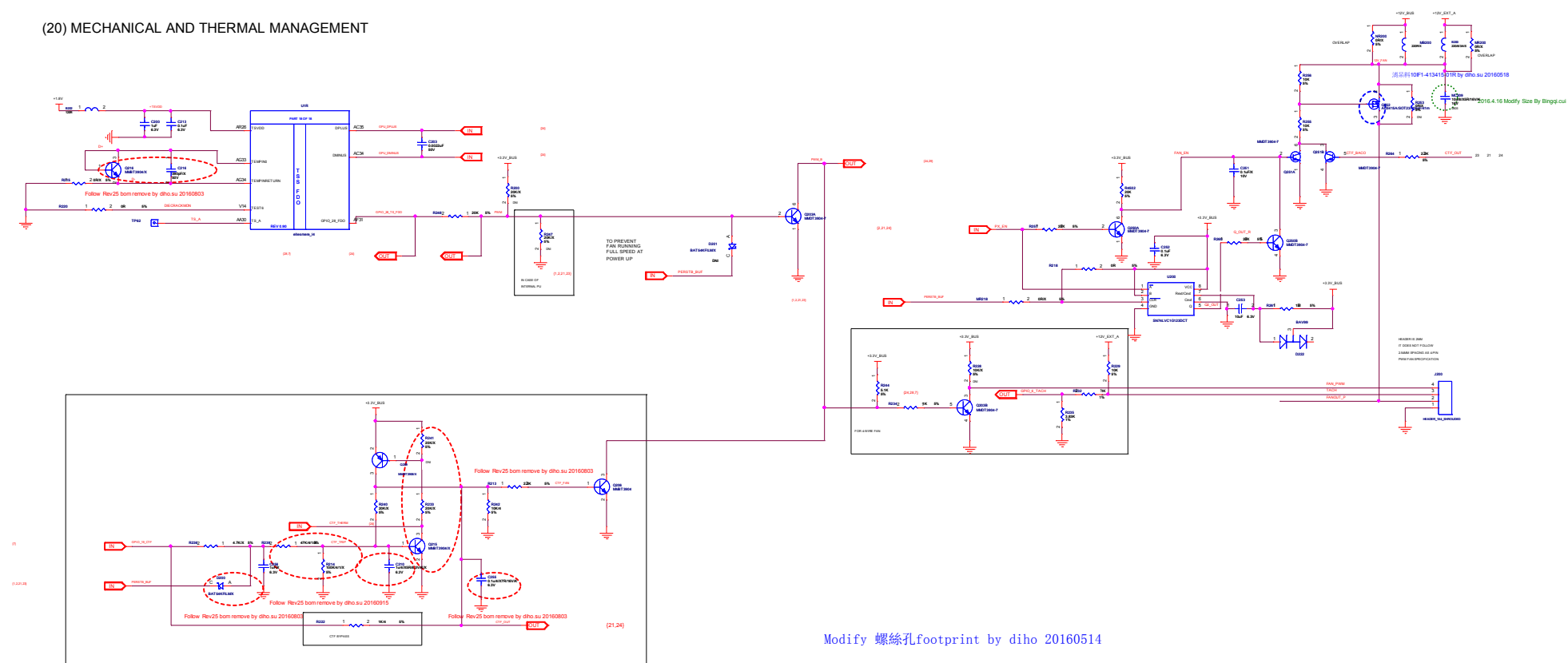
6PIN&8PIN CONNECT COLAY by diho 20160514



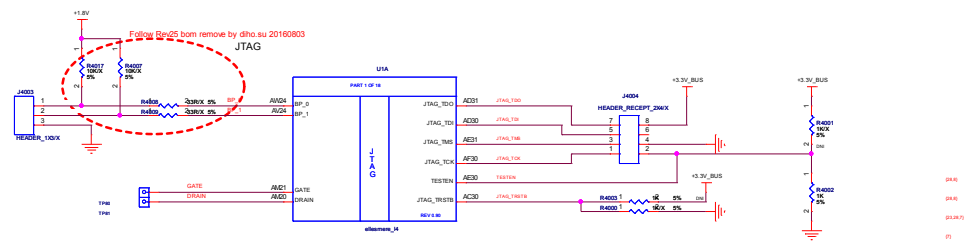
POWER UP SEQUENCE		
BUS 12V (20170101) +1.8V +0.80V	1.8V_BUS	1.8V_BUS_UP
BUS 12V (20170101) +1.8V +0.80V	1.8V_BUS	1.8V_BUS_DOWN



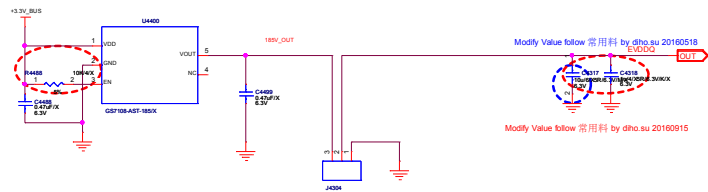
(20) MECHANICAL AND THERMAL MANAGEMENT



(21) DEBUG CIRCUITS



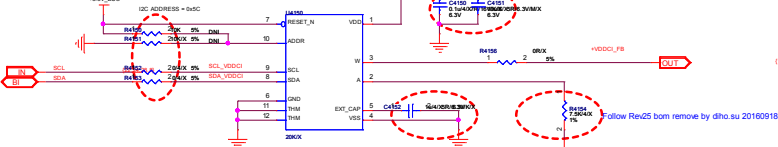
Follow Rev25 bom remove by diho.su 20160915



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DIGITAL POTS

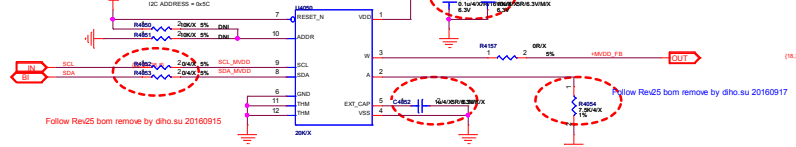
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DIGITAL POTS

+3.3V_BUS



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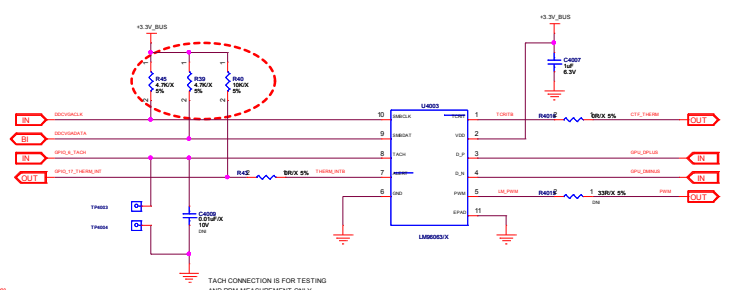
Follow Rev25 bom remove by diho.su 2016091



SWITCHES



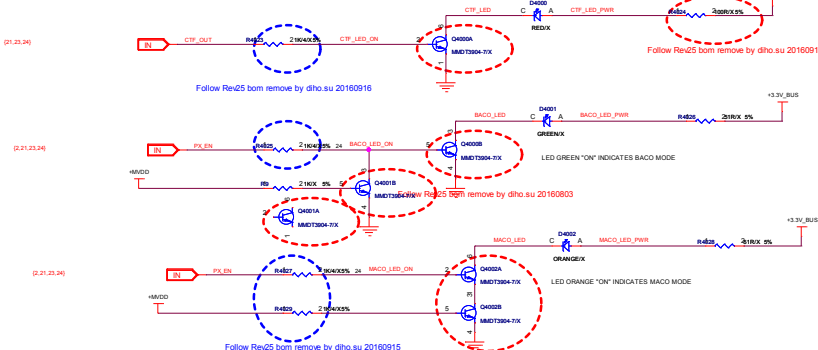
LM96163 FOR BACKUP THERMAL CONTROL



TACH CONNECTION IS FOR TESTING

LED LIGHTS

LED RED "ON" INDICATES CTF FAULT



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Follow Rev25 bom remove by diho.su 2016091

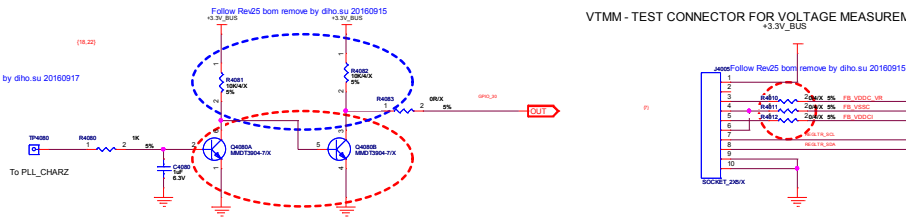
A circuit diagram showing a 5V DC voltage source connected in series with a 10k resistor and a 1N4148 diode. The diode is oriented with its cathode towards the positive terminal of the 5V source. The circuit is labeled with '5V', '10k', and '1N4148'.

Follow Re 25 to remove by diho.su 20160803

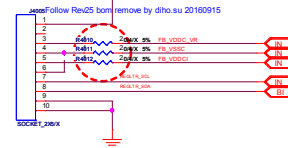
Follow DoOF from source by clicking on 20160014

VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS

+3.3V BUS



TO PULL CHARZ



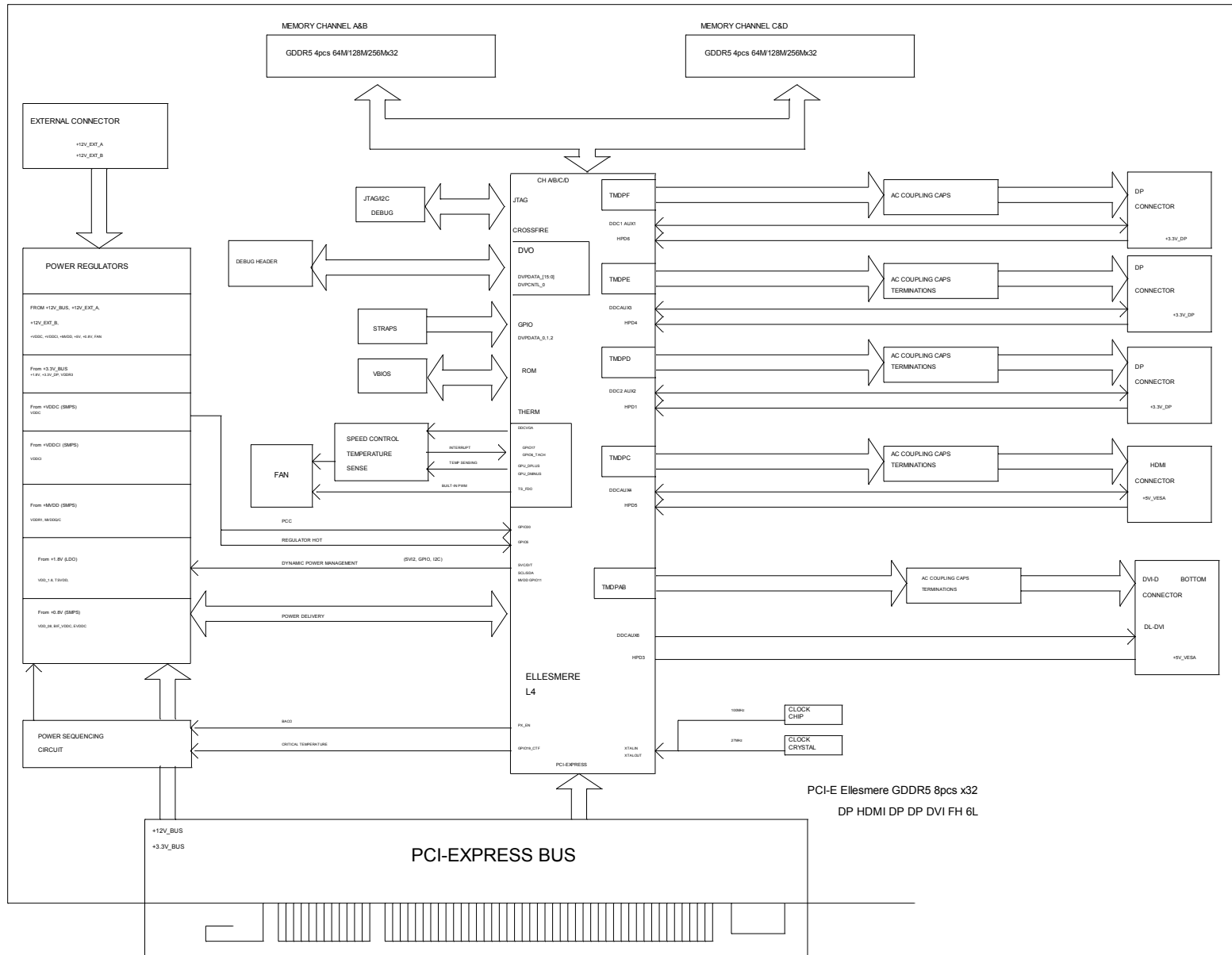
Follow Rev25 bom remove by diho.su 20160915

17

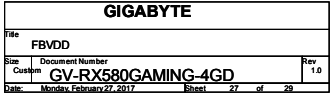
10

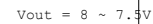
10

2)

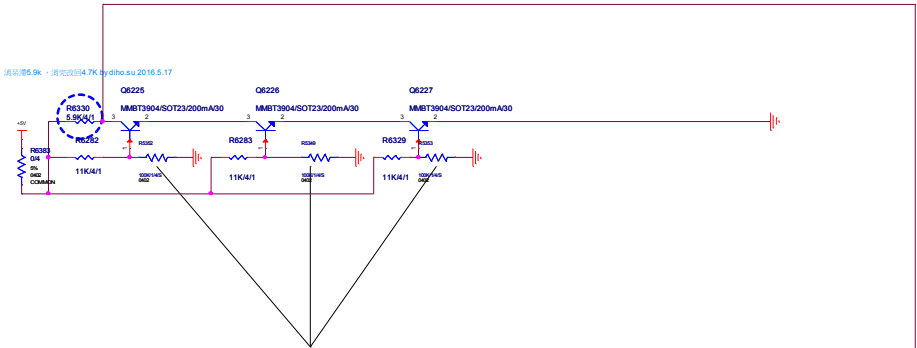


0	00A	0000000	
1	00B	0000000	1. Add GPIO1 for graphics VBIOS 2. update VBIOS driver sequence
2	00C	0000000	HDMI: - add register regulator R1380 - R1387 - add pull-down indicator L1380-L1387 remove C405, C406, VR405, C410, C414, R405
3	00D	0000000	Modify USB Design

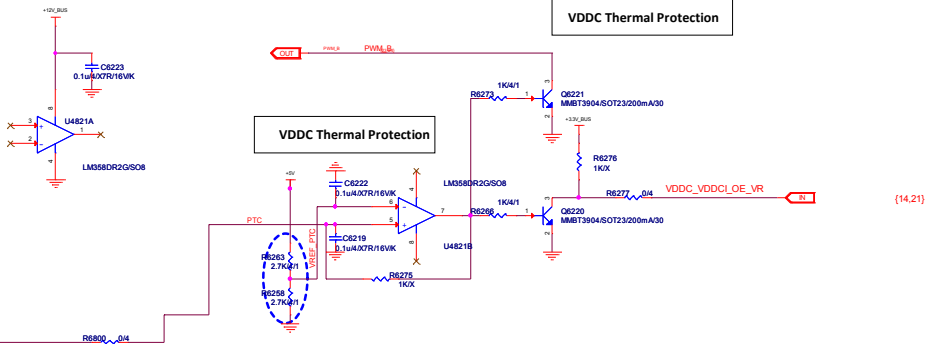




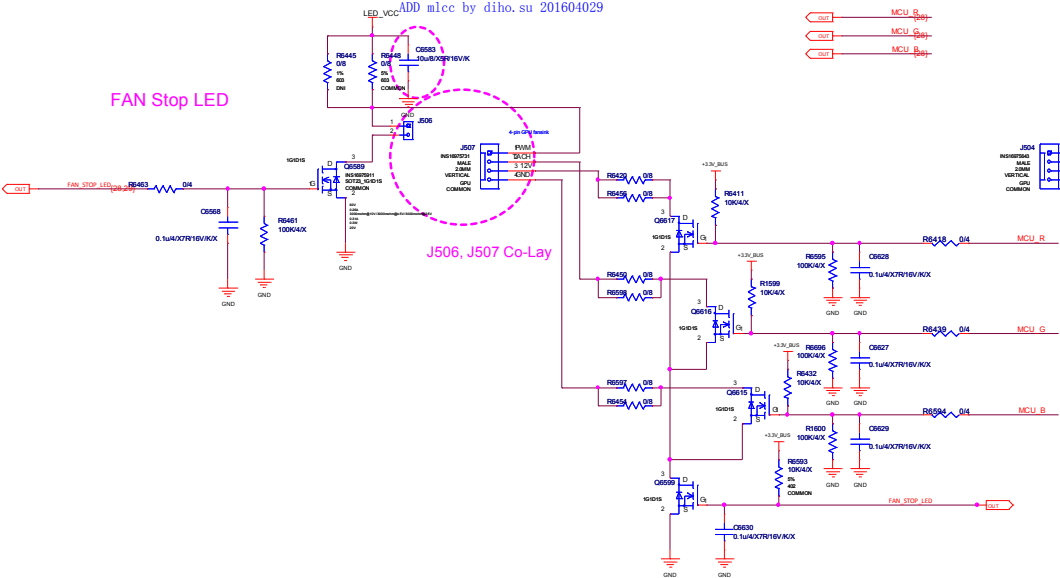
Add MOS VRHOT CIRCUIT



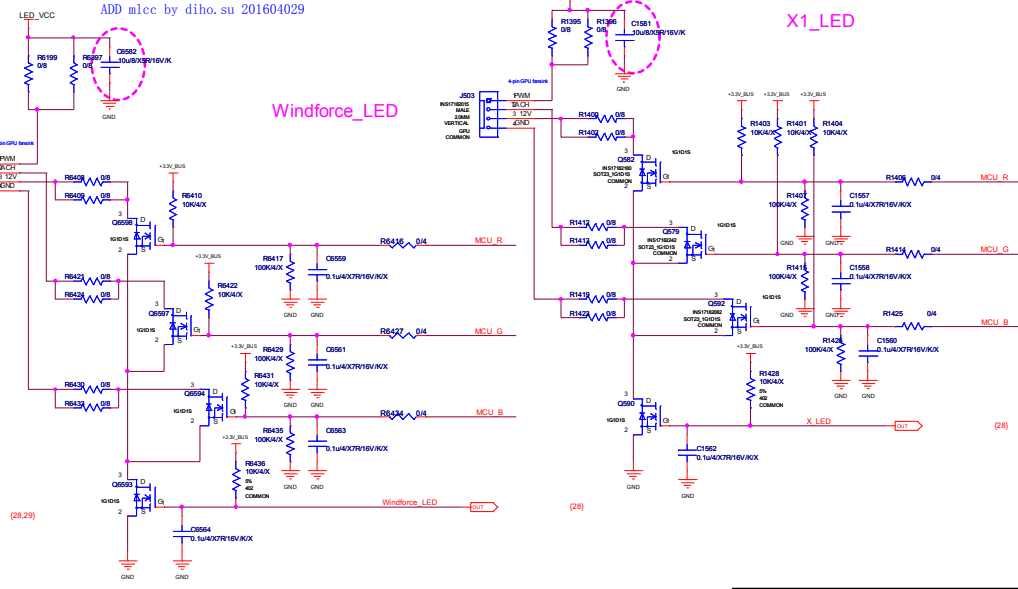
Near VDDC each high side MOS



FAN Stop LED



ADD mlcc by diho.su 201604029



X1_LED

